

FIG. 1

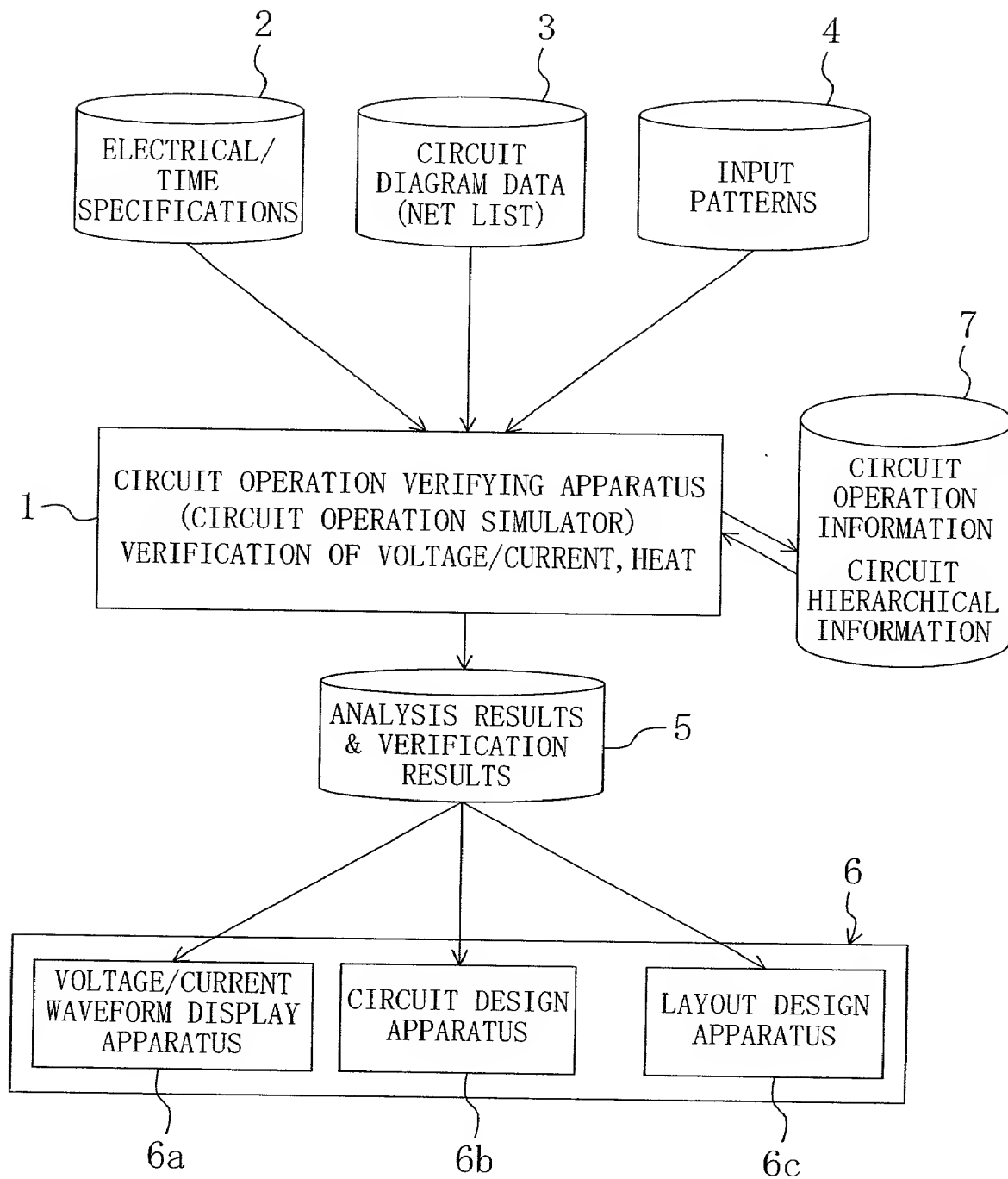


FIG. 2

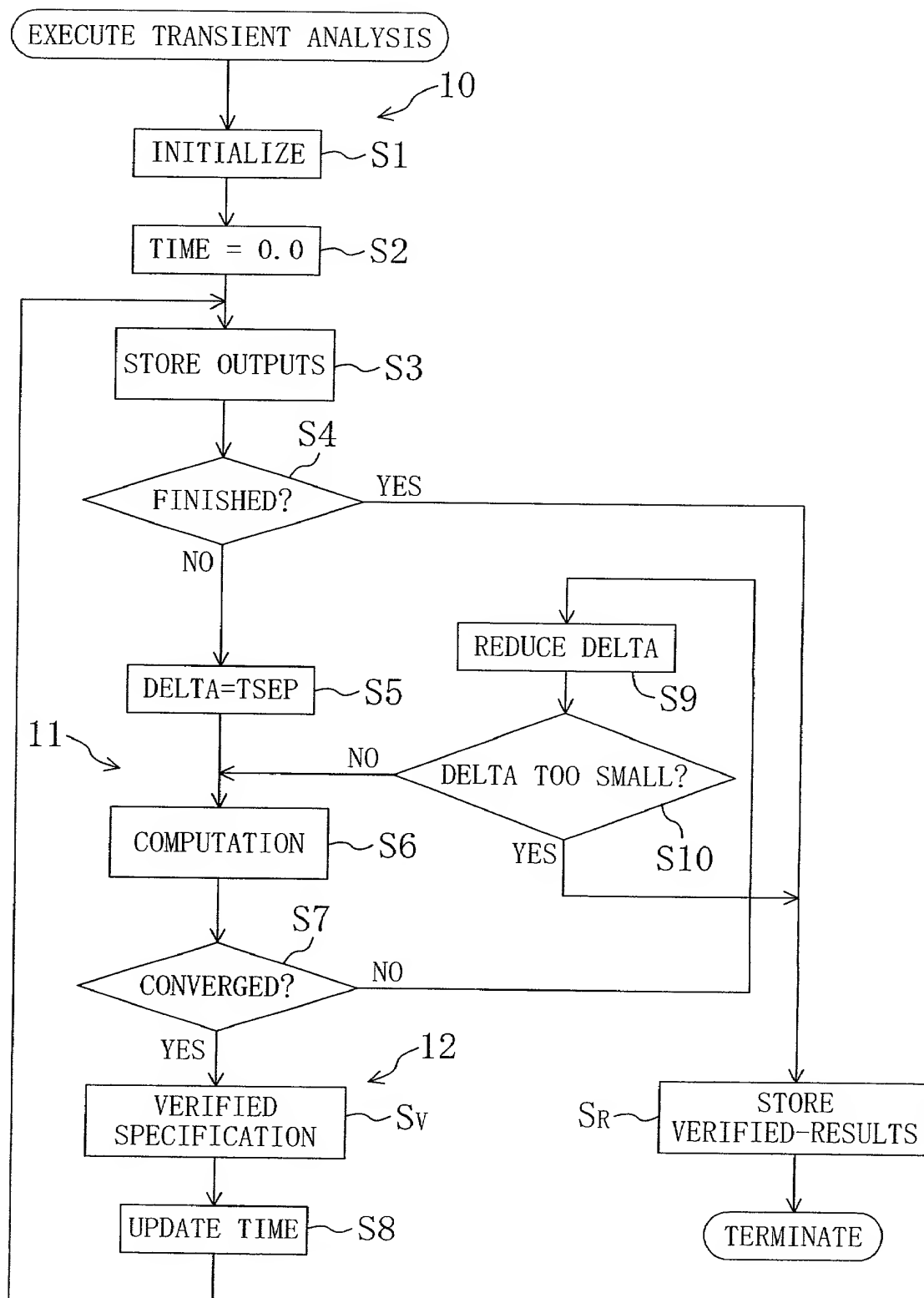


FIG. 3

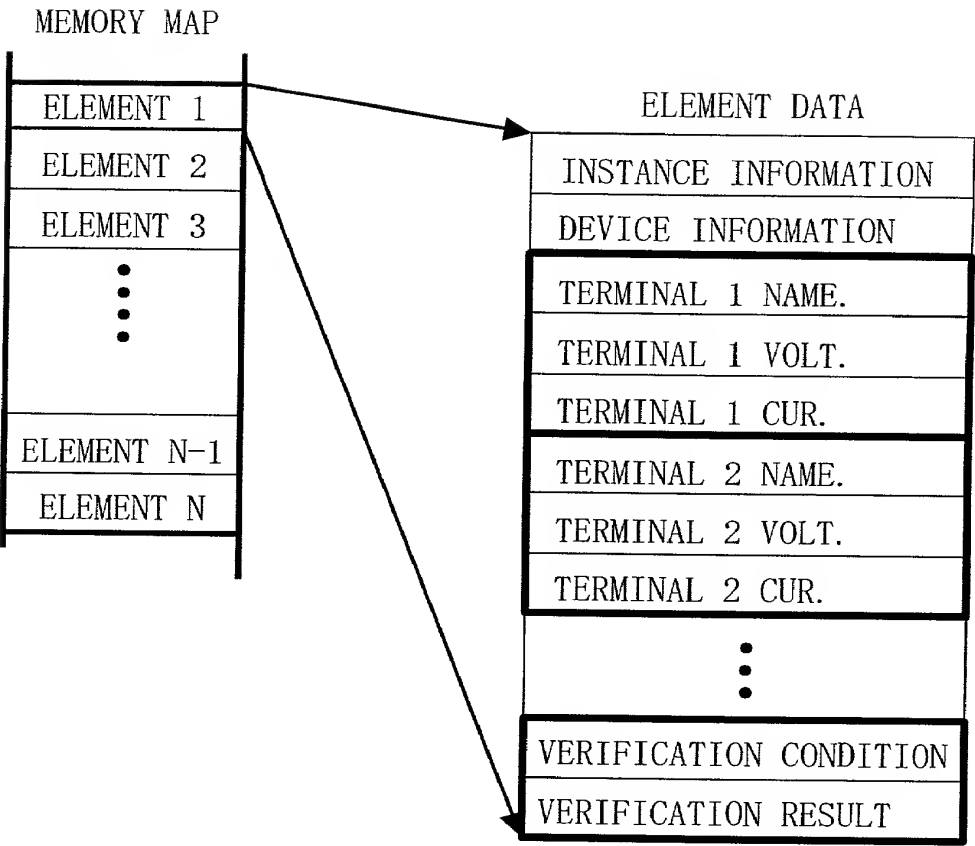


FIG. 4

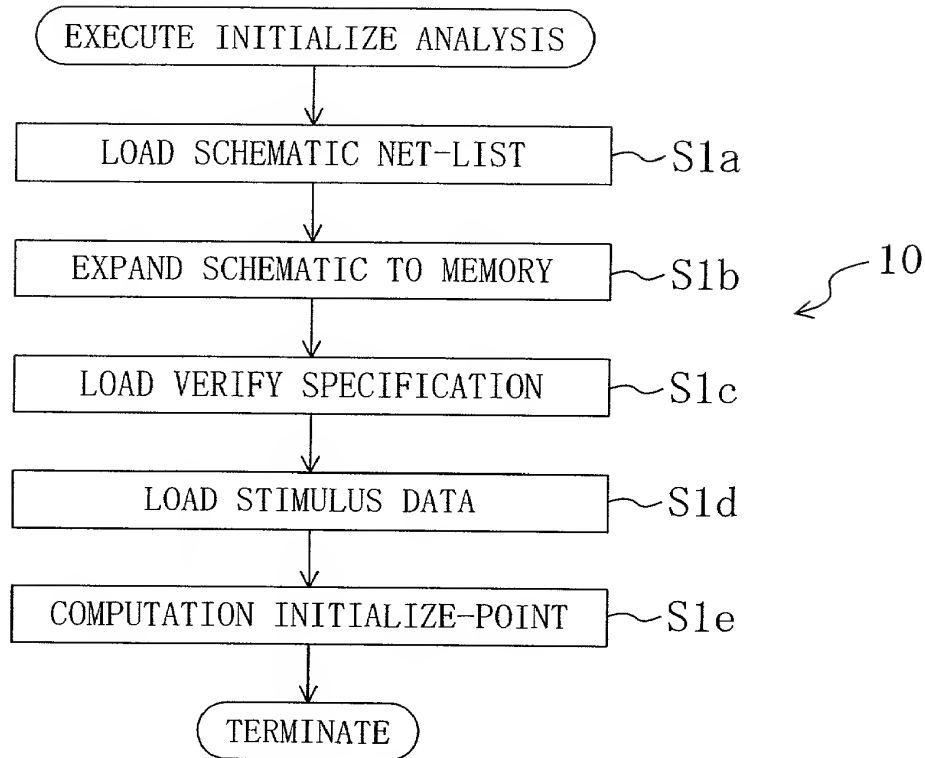


FIG. 5A

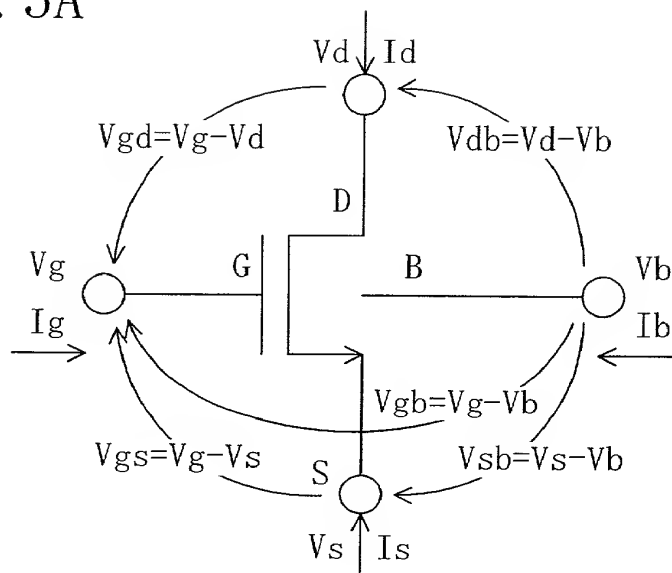


FIG. 5B

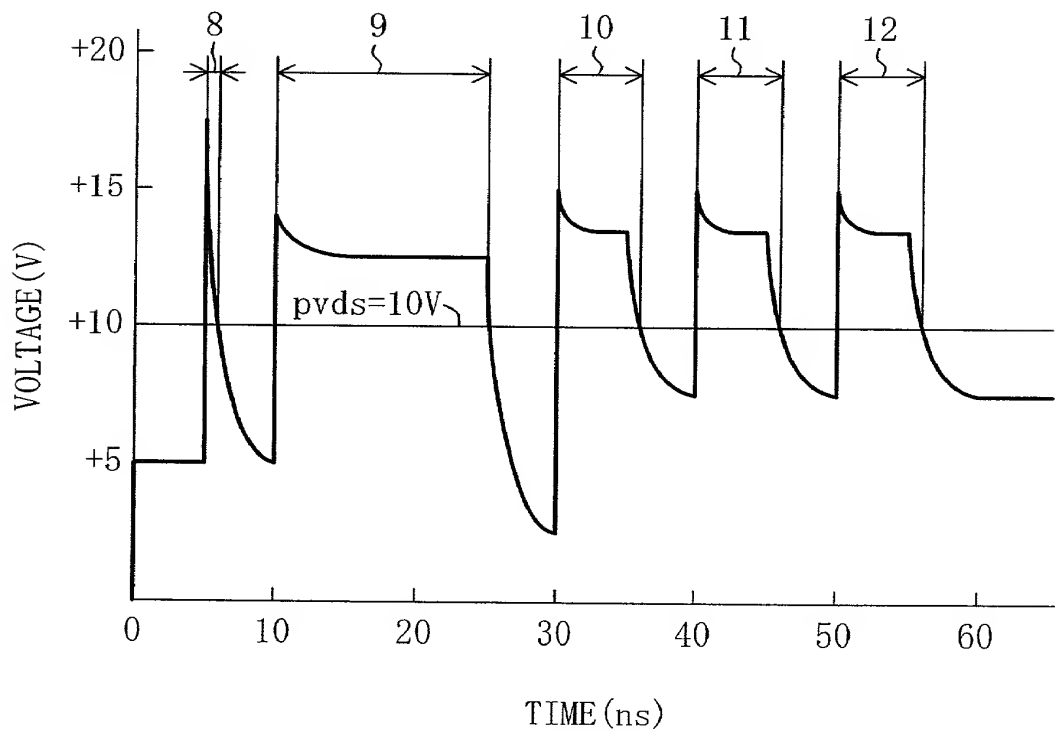


FIG. 6

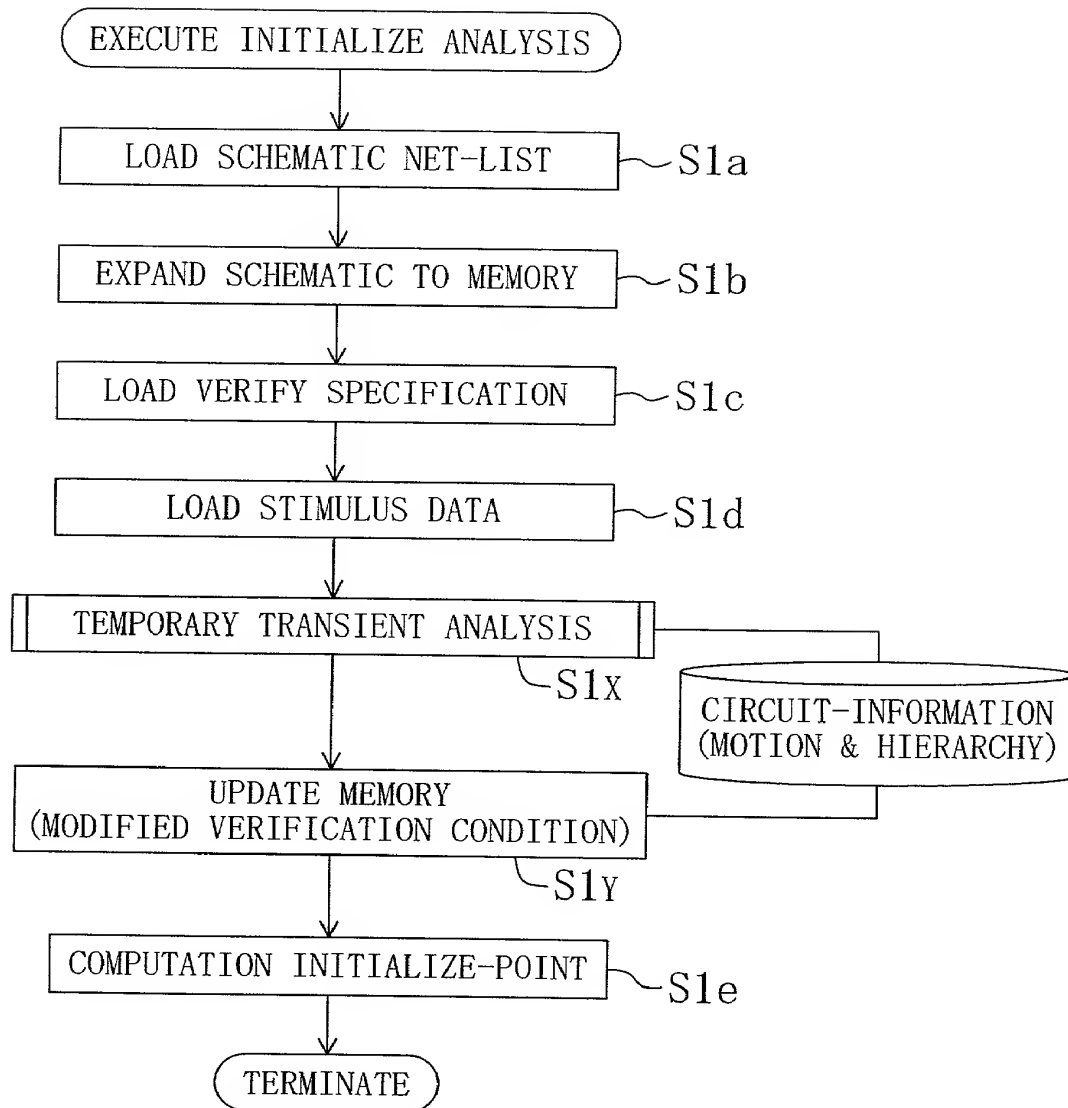


FIG. 7A

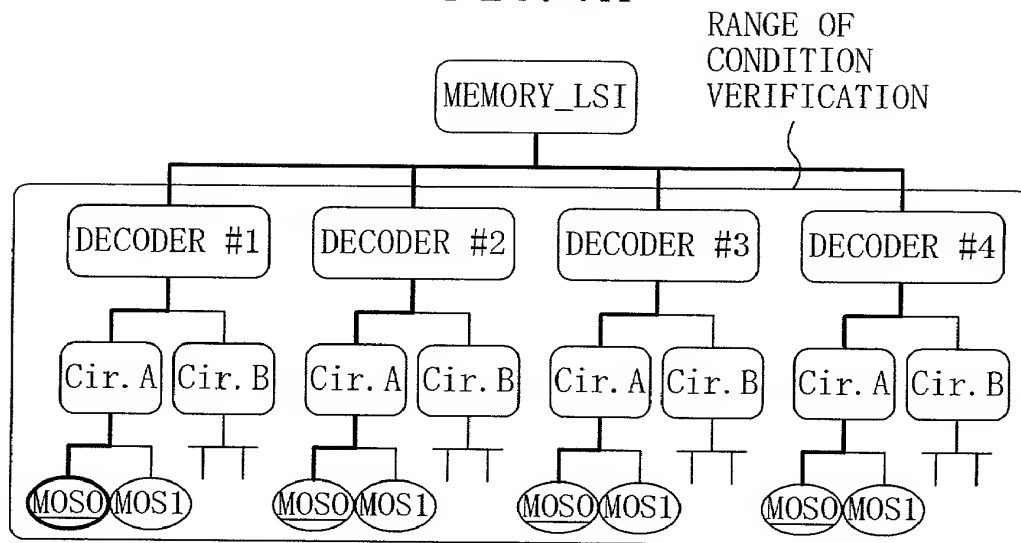


FIG. 7B

RANGE OF
CONDITION
VERIFICATION

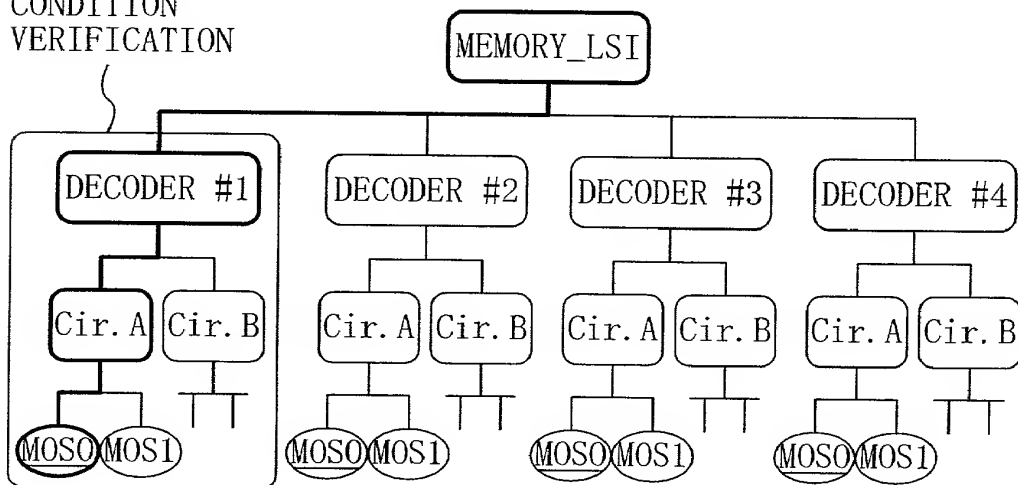


FIG. 8

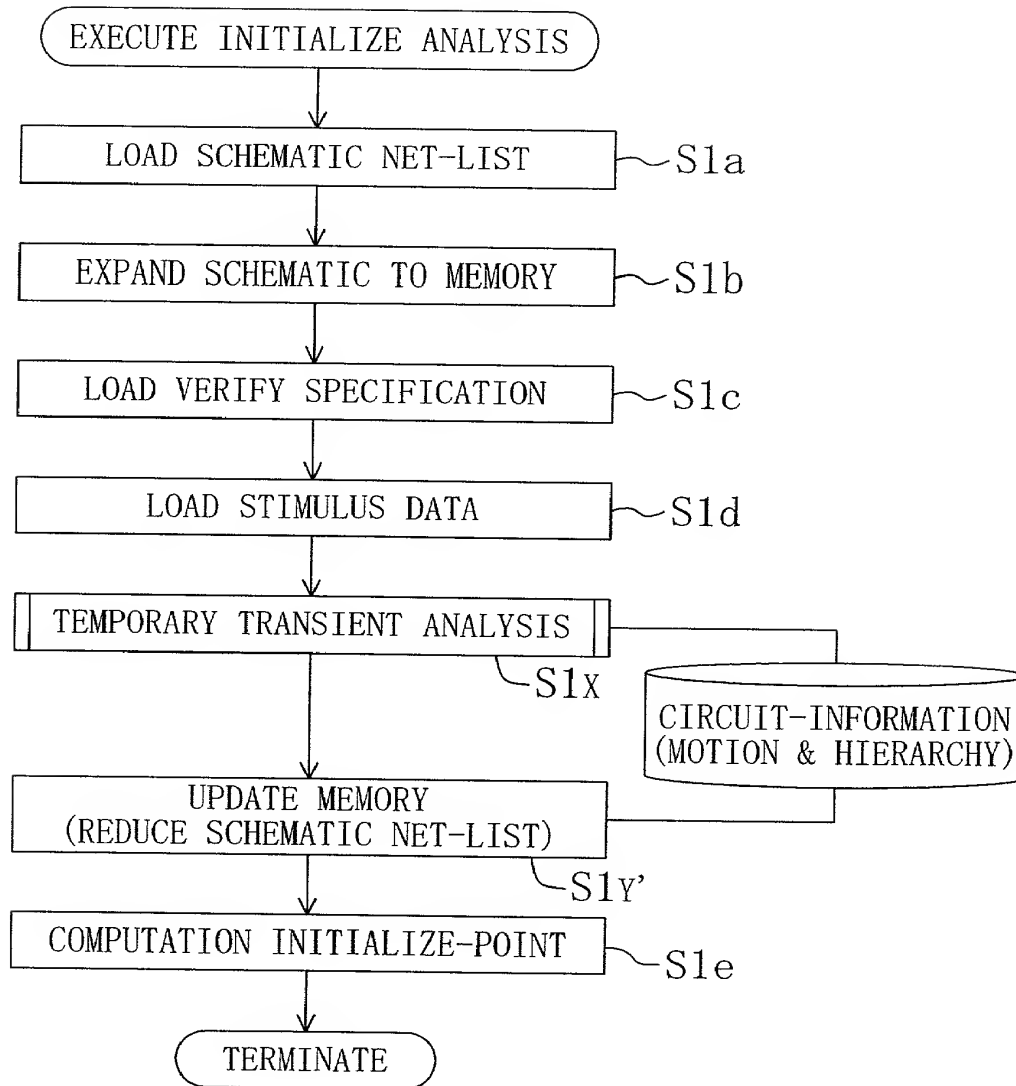


FIG. 9A

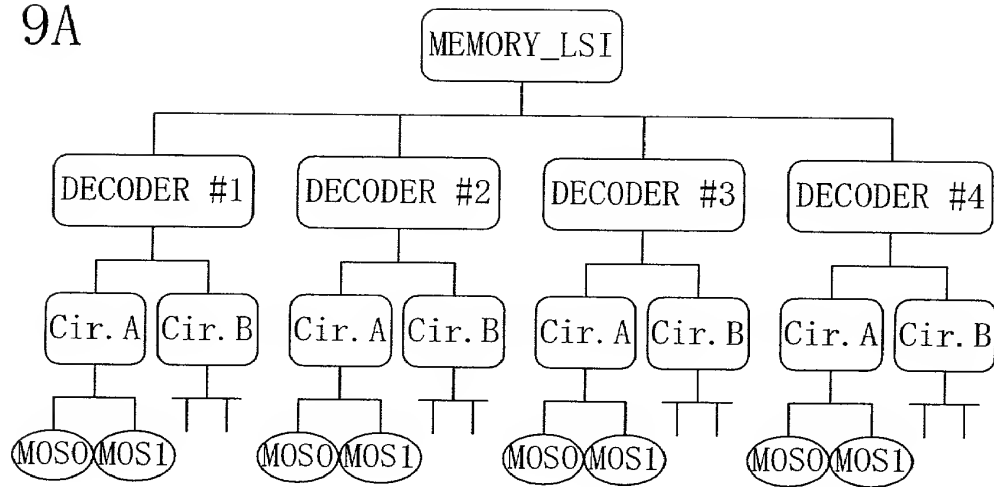


FIG. 9B

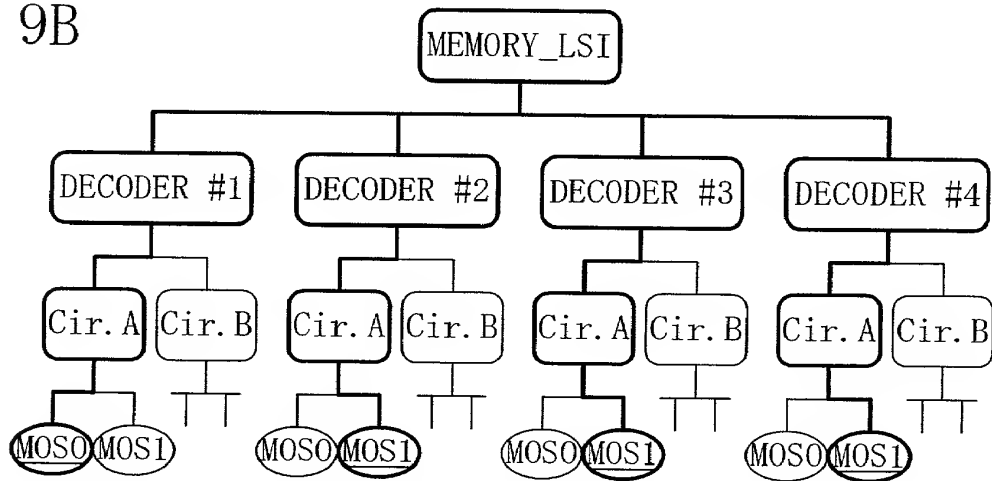


FIG. 9C

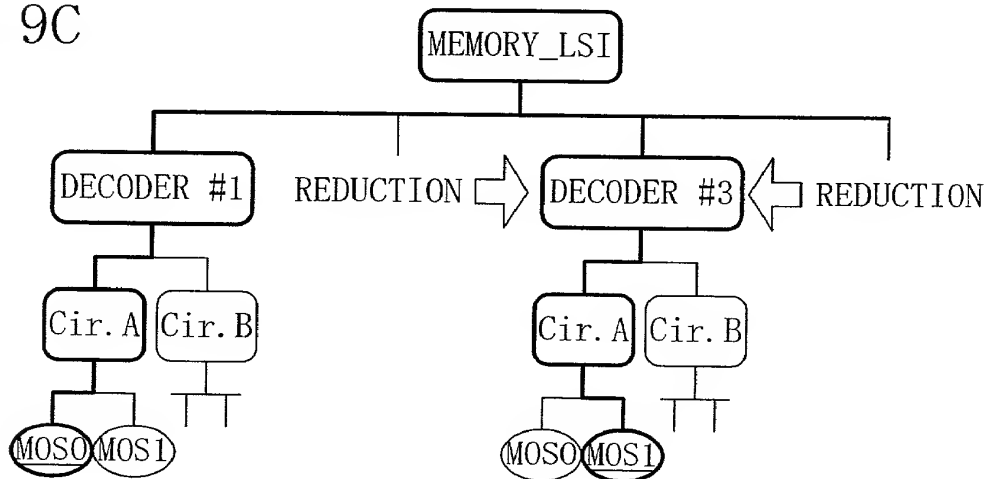


FIG. 10
PRIOR ART

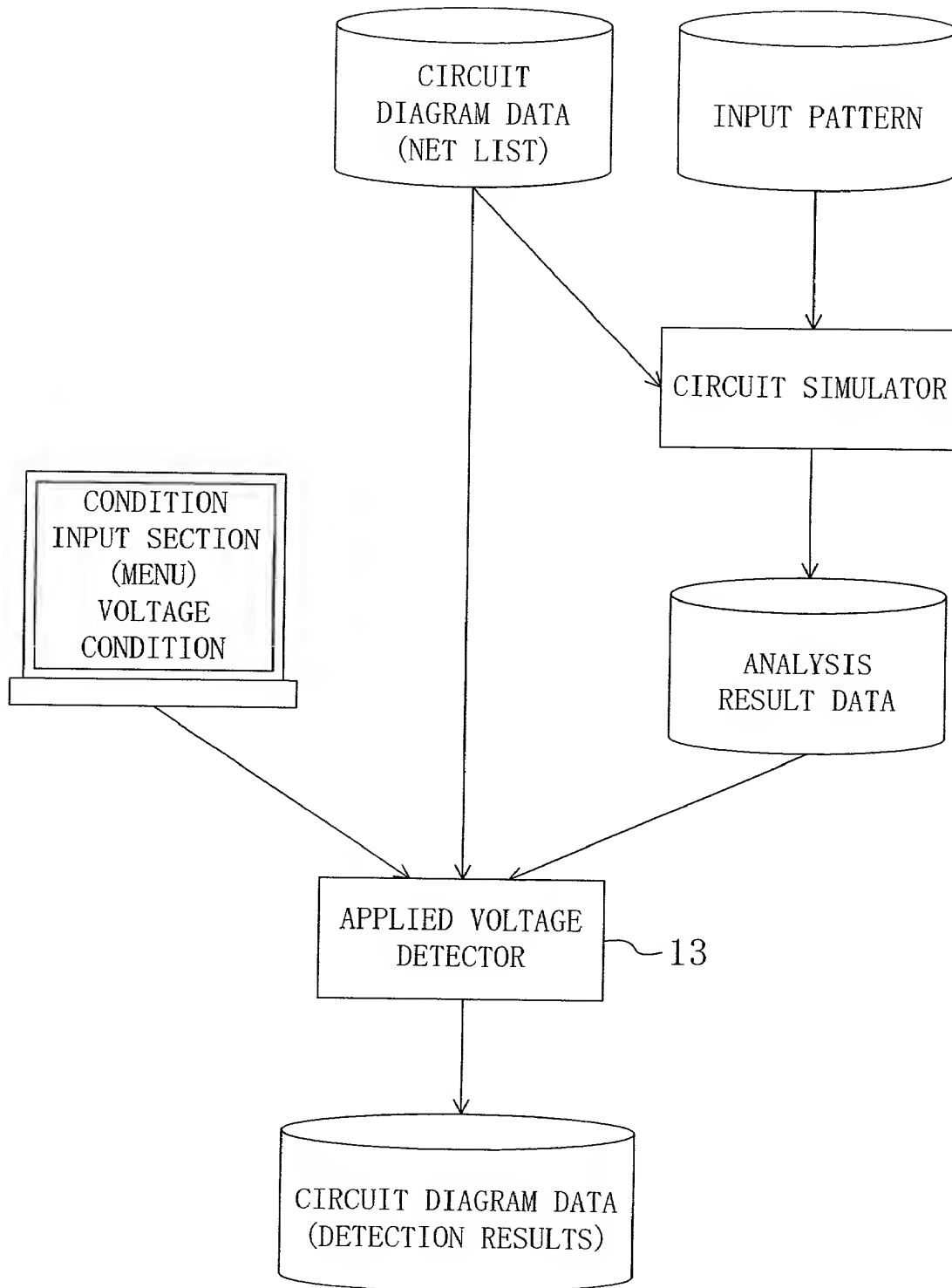


FIG. 11
PRIOR ART

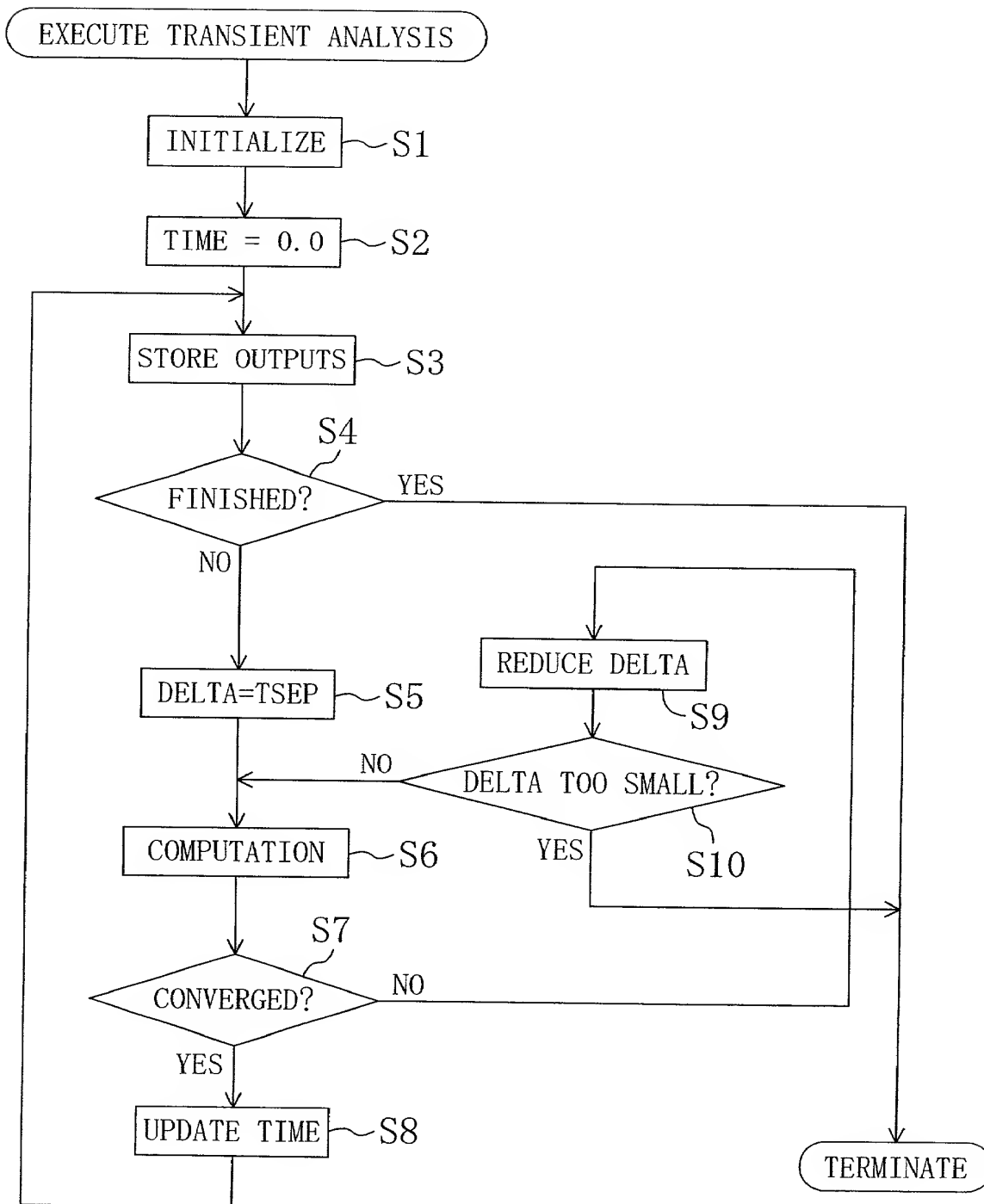


FIG. 12

PRIOR ART

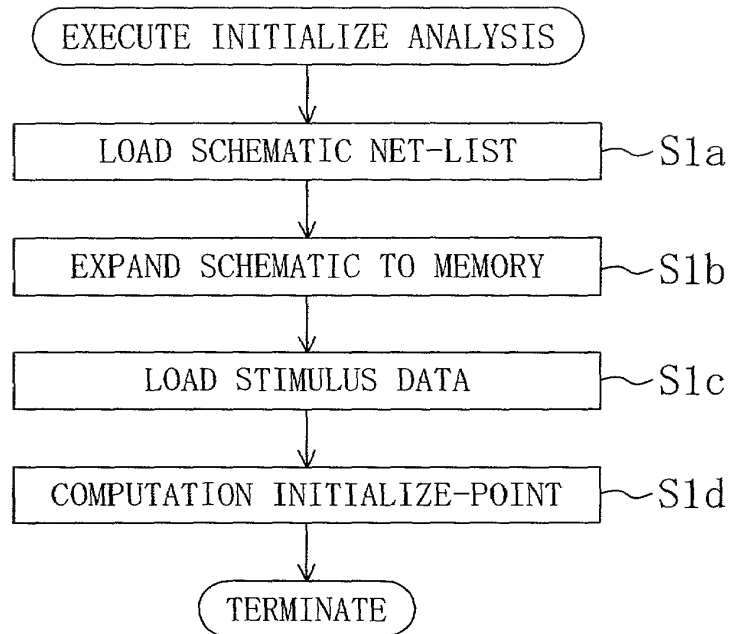


FIG. 13

PRIOR ART

